



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Makoto KUDO et al.

Group Art Unit: 2184

Application No.: 10/724,885

Filed: December 2, 2003

Docket No.: 104822.01

For: MICROCOMPUTER, ELECTRONIC EQUIPMENT AND DEBUGGING SYSTEM

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. References 1-4 were cited in a counterpart foreign application.
- 3. English language Abstracts of the non-English language references 1-3 are attached hereto. A partial English language translation of reference 4 is also attached.
- 4. A computer-generated English language translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See Reference 3.

Respectfully submitted,

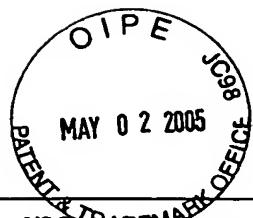
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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)			US DEPARTMENT OF COMMERCE PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		ATTY DOCKET NO. 104822.01		APPLICATION NO. 10/724,885	
					APPLICANT Makoto KUDO et al.			
					FILING DATE December 2, 2003		GROUP 2184	
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME			CLASS	SUB CLASS
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY			CLASS	SUB CLASS
	1	JP A 01-287752 (w/Abstract)	11/20/1989	Japan				
	2	JP A 02-264339 (w/Abstract)	10/29/1990	Japan				
	3	JP A 06-103104 (w/Abstract and English-language translation)	04/15/1994	Japan				
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)								
	4	Isao Yoshida, "Function and Application of Serial Port", Transistor Gijutsu, Vol. 32, No. 10, October 1, 1995, pp. 204-247 (w/partial English language translation of article)						
EXAMINER						DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								